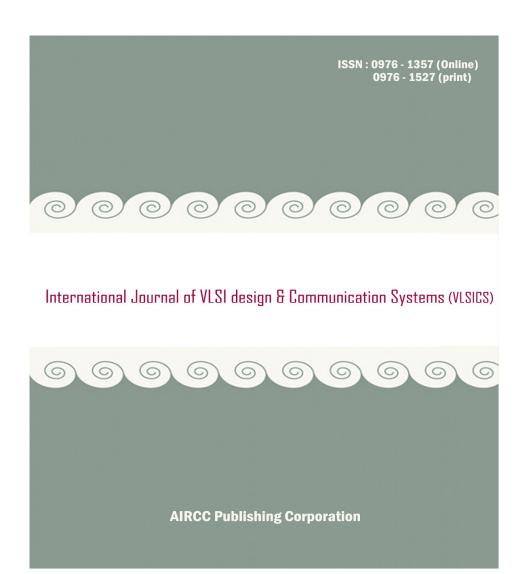
International Journal of VLSI Design & Communication Systems (VLSICS)

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SCOPE OF THE JOURNAL

The International journal of VLSI design & Communication Systems (VLSICS) is a bi monthly open access peer-reviewed journal that publishes articles which contribute new results in all areas of VLSI Design & Communications. The goal of this journal is to bring together researchers and practitioners from academia and industry to focus on advanced VLSI Design & communication concepts and establishing new collaborations in these areas.

Authors are solicited to contribute to this journal by submitting articles that illustrate research results, projects, surveying works and industrial experiences that describe significant advances in the VLSI design & Communications.

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- * Low Power and Power Aware Design
- * Testing, Reliability, Fault-Tolerance
- * Emerging Technologies
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